

## LIST OF EXPERIMENTS

CYCLE - 1	
Exp. No.	
1.1	Verification of Logic Gates
1.2	Half/Full Adder
1.3	Half/Full Subtractor
1.4	Binary - Gray and Gray - Binary Converter
1.5	MUX/DEMUX
1.6	
1.7	
1.8	
1.9	
1.10	
CYCLE - 2	
Exp. No.	
2.1	4 Bit Comparator
2.2	Encoder/Decoder
2.3	D Flipflop
2.4	4 Bit Counter
2.5	Shift Register
2.6	
2.7	
2.8	
2.9	
2.10	